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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10081821	FILING DATE 02/20/2002	CLASS 326	SUBCLASS 70	GAU 2819	EXAMINER DON W
<b>**APPLICANTS:</b> Stockstad Troy;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A CIP OF 09/711,486 11/13/2000 NOW U.S. PAT 6,429,685					
<b>FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>kl</i>				ATTORNEY DOCKET NO 130349	
TITLE : Integrated circuit and method of controlling output impedance					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		Assistant Examiner	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		Primary Examiner	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		PREPARED FOR ISSUE	Application Examiner	
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